

## Jeu d'instructions

### MANIPULATIONS DE REGISTRES

TO, PD	<b>NOP</b>	-	No Operation
	<b>SLEEP</b>	-	Go into Standby mode
Z	<b>TSTF</b>	f	Test File
Z	<b>CLRF</b>	f	Clear f
Z	<b>CLRWF</b>	-	Clear W
TO, PD	<b>CLRWDWT</b>	-	Clear Watchdog Timer
Z	<b>MOVF</b>	f, d	Move f
	<b>MOVFW</b>	f	Move File to W
	<b>MOVLW</b>	k	Move literal to W
	<b>MOVWF</b>	f	Move W to f
	<b>SWAPF</b>	f, d	Swap nibbles in f

### CALCULS

Z	<b>DECF</b>	f, d	Decrement f
Z	<b>INCF</b>	f, d	Increment f
Z	<b>ADDWF</b>	f, d	Add W to f
Z	<b>ADDWFC</b>	f, d	Add W to f with Carry
C, DC	<b>ADDLW</b>	k	Add literal and W
C, DC	<b>ADDWF</b>	f, d	Add W and f
	<b>SUBWF</b>	f, d	Subtract W from f
	<b>SUBWFC</b>	f, d	Subtract W from f with Carry
C, DC	<b>SUBLW</b>	k	Subtract W from literal
C, DC	<b>SUBWF</b>	f, d	Subtract W from f
Z	<b>NEGF</b>	f, d	Negate File
C	<b>RLF</b>	f, d	Rotate Left f through Carry
C	<b>RRF</b>	f, d	Rotate Right f through Carry

### FONCTIONS LOGIQUES

Z	<b>ANDLW</b>	k	AND literal with W
Z	<b>ANDWF</b>	f, d	AND W with f
Z	<b>COMF</b>	f, d	Complement f
Z	<b>IORLW</b>	k	Inclusive OR literal with W
Z	<b>IORWF</b>	f, d	Inclusive OR W with f
Z	<b>XORLW</b>	k	Exclusive OR literal with W
Z	<b>XORWF</b>	f, d	Exclusive OR W with f

### SAUTS

B	<b>BR</b>	k	Branch
	<b>GOTO</b>	k	Go to address
	<b>LGOTO</b>	k	Long Goto
	<b>DECFSZ</b>	f, d	Decrement f, Skip if 0
	<b>INCFSZ</b>	f, d	Increment f, Skip if 0
	<b>BTFSZ</b>	f, b	Bit Test f, Skip if Set
	<b>BTFSS</b>	f, b	Bit Test f, Skip if Set
BC	<b>BC</b>	k	Branch on Carry
BDC	<b>BDC</b>	k	Branch on Digit Carry
BNC	<b>BNC</b>	k	Branch on No Carry
BNDC	<b>BNDC</b>	k	Branch on No Digit Carry
BNZ	<b>BNZ</b>	k	Branch on No Zero
BZ	<b>BZ</b>	k	Branch on Zero
	<b>SKPC</b>		Skip on Carry
	<b>SKPDC</b>		Skip on Digit Carry
	<b>SKPNC</b>		Skip on No Carry
	<b>SKPNDC</b>		Skip on No Digit Carry
	<b>SKPNZ</b>		Skip on No Zero
	<b>SKPZ</b>		Skip on Zero

### SOUS-PROGRAMMES

	<b>CALL</b>	k	Call Subroutine
	<b>LCALL</b>	k	Long Call
	<b>RETURN</b>	-	Return from Subroutine
	<b>RETLW</b>	k	Return with literal in W
	<b>RETFIE</b>	-	Return from interrupt

### MANIPULATIONS DE BITS

	<b>BCF</b>	f, b	Bit Clear f
	<b>BSF</b>	f, b	Bit Set f
	<b>CLRC</b>		Clear Carry
	<b>CLRDC</b>		Clear Digit Carry
	<b>CLRZ</b>		Clear Zero
	<b>SETC</b>		Set Carry
	<b>SETDC</b>		Set Digit Carry
	<b>SETZ</b>		Set Zero

## 12F683

Alimentation : 2V - 5V

Horloge : 0—20 MHz  
(interne : 125 kHz—8 MHz)

4 CAN 10 bits

1 comparateur

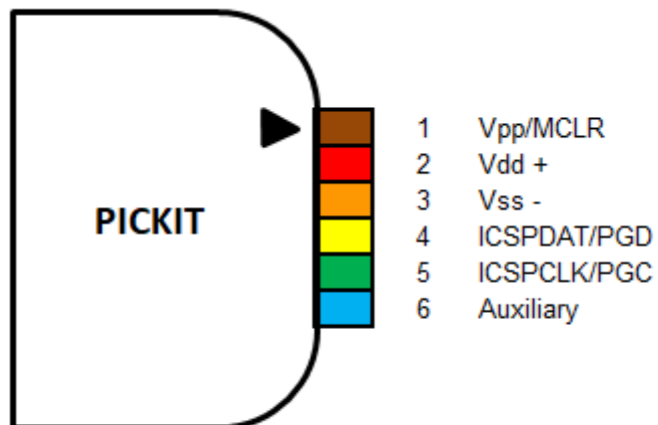
2 Timers

MLI (PWM) 10 bits

EEPROM 256 octets

2048 mots de programme

	BANK 0	BANK 1
Entrées TOR	Indirect addr.(1)	
	PCL	
	STATUS	
	FSR	
Horloge	PCLATH	
	INTCON	
	GPIO	OPTION_REG
		TRISIO
Timers		WPU
	PIR1	PIE1
		IOC
	WDTCN	PCON
MLI		OSCCON
		OSCTUNE
	TMRO	
	TMR1L	
Entrées Analogiques	TMR1H	
	T1CON	
	TMR2	PR2
	T2CON	
EEPROM	CCPR1L	
	CCPR1H	
	CCP1CON	
	CMCON0	VRCON
	CMCON1	ANSEL
	ADRESH	ADRESL
	ADCON0	
		EEDAT
		EEADR
		EECON2
	Registres généraux 80 octets	Registres généraux 32 octets
	Registres communs 16 octets	



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